

What is claimed is:

1. A liquid crystal display device comprising, in a liquid crystal side pixel region of one substrate of respective substrates as disposed to oppose each other with a layer of liquid crystal material interposed therebetween, a thin-film transistor which is driven by a scan signal from a gate signal line; a pixel electrode to which an image signal from a drain signal is supplied via this thin-film transistor; a protective film formed to also cover said thin-film transistor and the pixel electrode; and a resin film formed on or over an upper surface of this protective film.
2. The liquid crystal display device as recited in claim 1, wherein a counter electrode for generation of an electric field between itself and the pixel electrode and a counter voltage signal line for supplying a counter voltage thereto are formed on the one substrate side.
3. The liquid crystal display device as recited in claim 1, wherein a counter electrode forming a pair together with said pixel electrode is formed at an under layer of a dielectric film for use as a gate insulation film of said thin-film transistor.
4. The liquid crystal display device as recited in claim 1, wherein said resin layer is 1,000 nm or less in layer thickness thereof.
5. The liquid crystal display device as recited in claim 1, wherein the protective film is such that a contact hole is not

formed in the pixel region.

6. The liquid crystal display device as recited in claim 1, wherein at least one of the gate signal line and drain signal line is comprised of either aluminum or a material containing it, its terminal section is exposed from said protective film, and a transparent conductive material layer is formed at such exposure portion.

7. The liquid crystal display device as recited in claim 2, wherein the counter voltage signal line is comprised of either aluminum or a material containing it, its terminal section is exposed from said protective film, and a transparent conductive material layer is formed at such exposure portion.

8. The liquid crystal display device as recited in claim 6 or 7, wherein the transparent conductive material layer comprises an ITO film.

9. A liquid crystal display device comprising, in a liquid crystal side pixel region of one substrate of respective substrates as disposed to oppose each other with a layer of liquid crystal material interposed therebetween, a thin-film transistor which is driven by a scan signal from a gate signal line; a pixel electrode to which an image signal from a drain signal is supplied via this thin-film transistor; a protective film formed to also cover said thin-film transistor and the pixel electrode; and a resin film formed on or over an upper surface of this protective film, wherein:

a counter electrode for generation of an electric field between itself and said pixel electrode and a counter voltage signal line for supplying a counter voltage thereto are formed on the one substrate side, and

a metal material layer is not formed in a display region which is an ensemble of said pixel regions at an upper layer than said resin layer of the substrate with said resin film formed thereon.

10. The liquid crystal display device as recited in claim 9, wherein an electrode made of a conductive material is not formed in said display region at an upper layer than said resin layer of the substrate with said resin film formed thereon.

11. The liquid crystal display device as recited in claim 9, wherein those other than an orientation film are not formed in said display region at an upper layer than said resin layer of the substrate with said resin film formed thereon.

12. The liquid crystal display device as recited in claim 9, wherein those other than an orientation film are not formed in said display region at an upper layer than said resin layer of the substrate with said resin film formed thereon.

13. A liquid crystal display device comprising, in a liquid crystal side pixel region of one substrate of respective substrates as disposed to oppose each other with a layer of liquid crystal material interposed therebetween, a gate signal line made of a metal, a drain signal line made of a metal being

formed over the metallic gate signal line with a dielectric film sandwiched therebetween, a thin-film transistor as driven by a scan signal from said metallic gate signal line, a pixel electrode to which an image signal from said metallic drain signal line is supplied via this thin-film transistor; a protective film formed to also cover said thin-film transistor and said metallic drain electrode plus said pixel electrode; and a resin film formed on or over an upper surface of this protective film, wherein

a counter electrode for generation of an electric field between it and said pixel electrode and a counter voltage signal line made of a metal for supplying a counter voltage thereto are formed on the one substrate side with the metallic counter voltage signal line being formed at the same layer as said scan signal line, and

at least in a display region being an ensemble of said pixel regions, at least three layers of said protective film and resin film plus orientation film are arranged to separate all metal layers from a liquid crystal layer to thereby prevent contact between liquid crystals and metallic material(s).